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14 TABLE 1.2 Fundamentals of Digital
Logic and Microcomputer Design

Comparison of output characteristics of
LS-TTL, nMOS, HC, and HCT IOH VOL IOL
LS-TTL... dissipation (in mW) of a gate.

1.2 Design Levels Three design levels
can be defined for digital systems:

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systems level, logic level, and device level.

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